Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	8491	(lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 15:48
S26	1	09/900,945	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:11
S27	2	"6523136".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:11
S28	90	(((((((((714/30 or 714/25 or 714/733 or 714/734 or 700/26). ccls.) and chip) and (processor or controller)) and (program with count$3)) and register) and (peripheral or function$3)) and (select$4 or multiplex$3)) and (select$4 with control$3)) and @ad<"20010710"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:46
S29	91	((((lsi or(logic adj circuit) or (integrated adj circuit) or ic or MPU or microprocessror or processor) and ((peripheral or auxilliary) adj circuit))) and debug) and @ad<"20000710"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:45
S30	24	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessor) and ((peripheral or auxilliary) adj circuit)) and (one adj chip)) and debug	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:45
S31	9	((((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and (one adj chip)) and debug) and @ad<"20000710"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:45
S32	20	("5544311" or "5838692" or "5566303" or "6523136" or "5614818" or "5416919" or "6687857" or "5566300" or "5951696" or "5771240").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:48
S33	2	"20020013918"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:48

S34	22	S32 or S33	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/10 16:48